

FIG. 1a
(Prior Art)

100

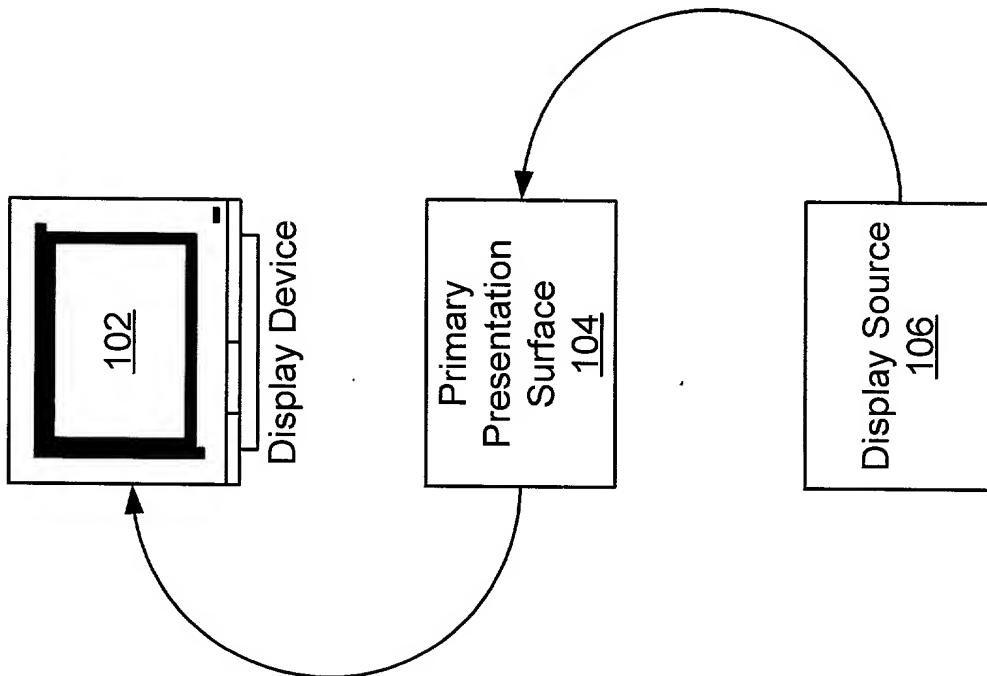
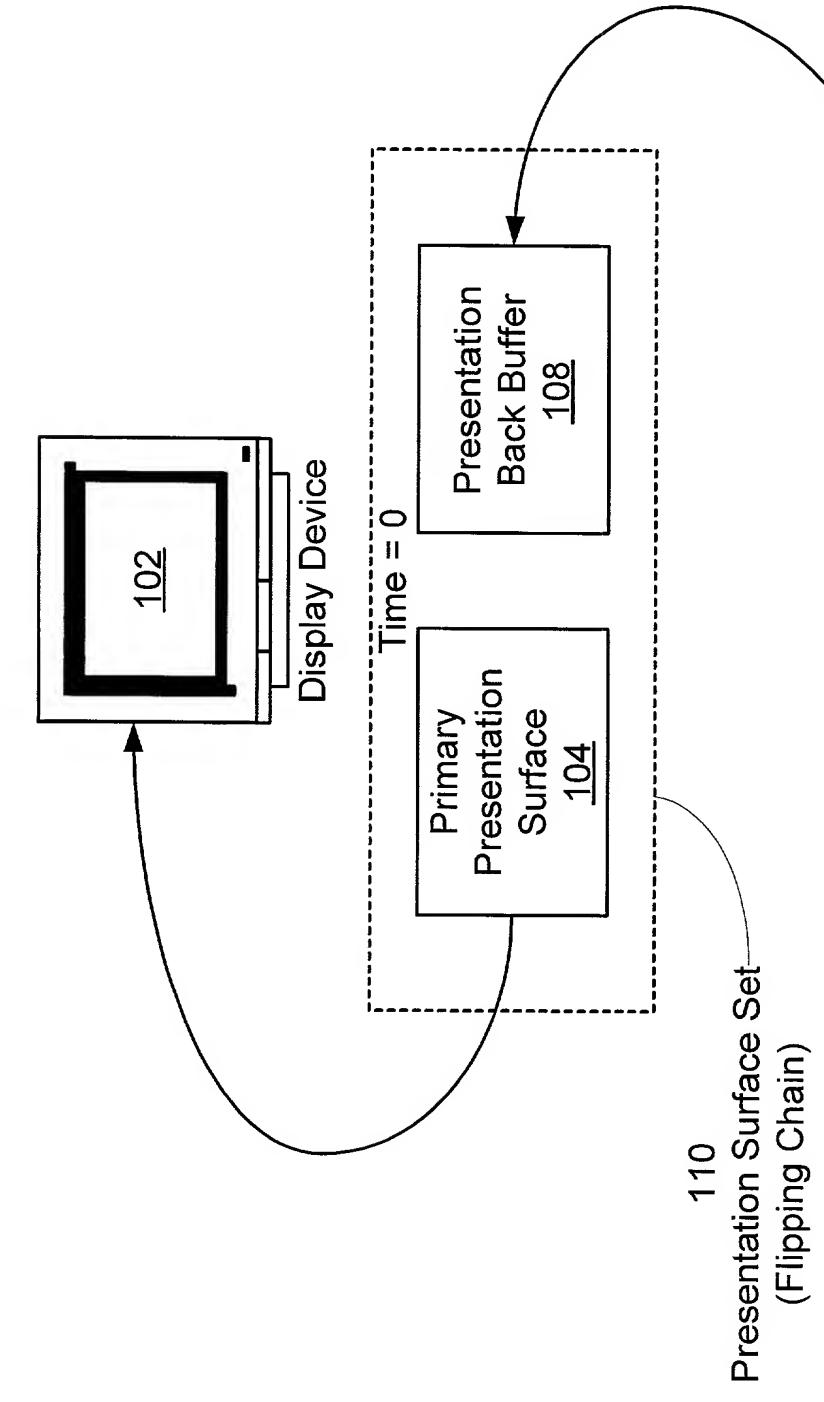


FIG. 1b (Prior Art)

100



110
Presentation Surface Set
(Flipping Chain)

FIG. 1c
(Prior Art)

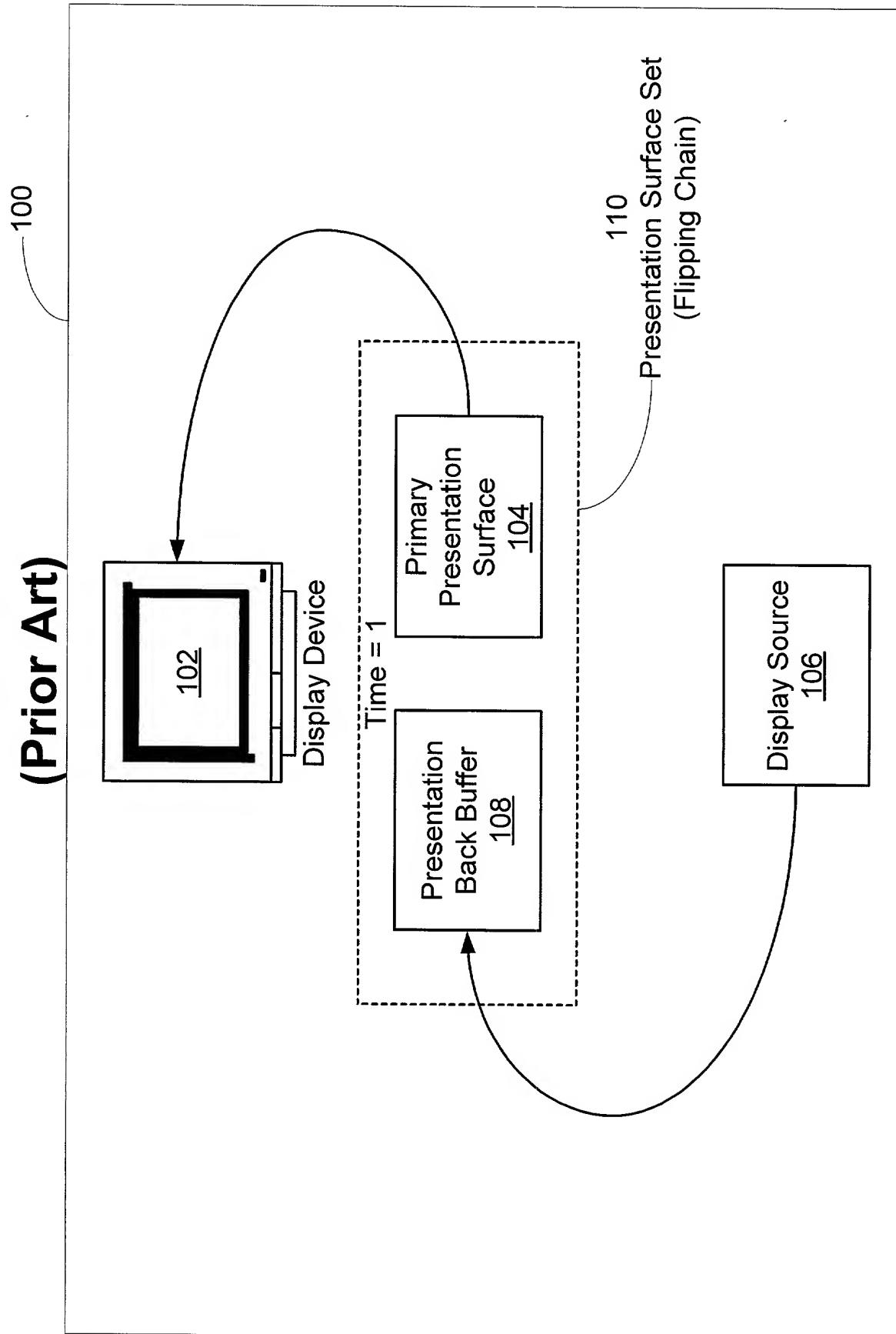
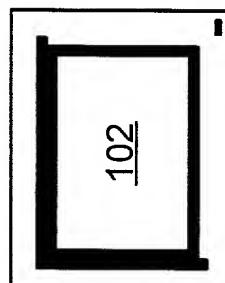


FIG. 1d (Prior Art)

100



Display Device

110
Presentation Surface Set
(Flipping Chain)

Primary
Presentation
Surface
104

Presentation
Back Buffer
108

112
Memory Surface Set
(Flipping Chain)

Display Source 106

Back Buffer
114

Ready Buffer
116

100

FIG. 1e (Prior Art)

100

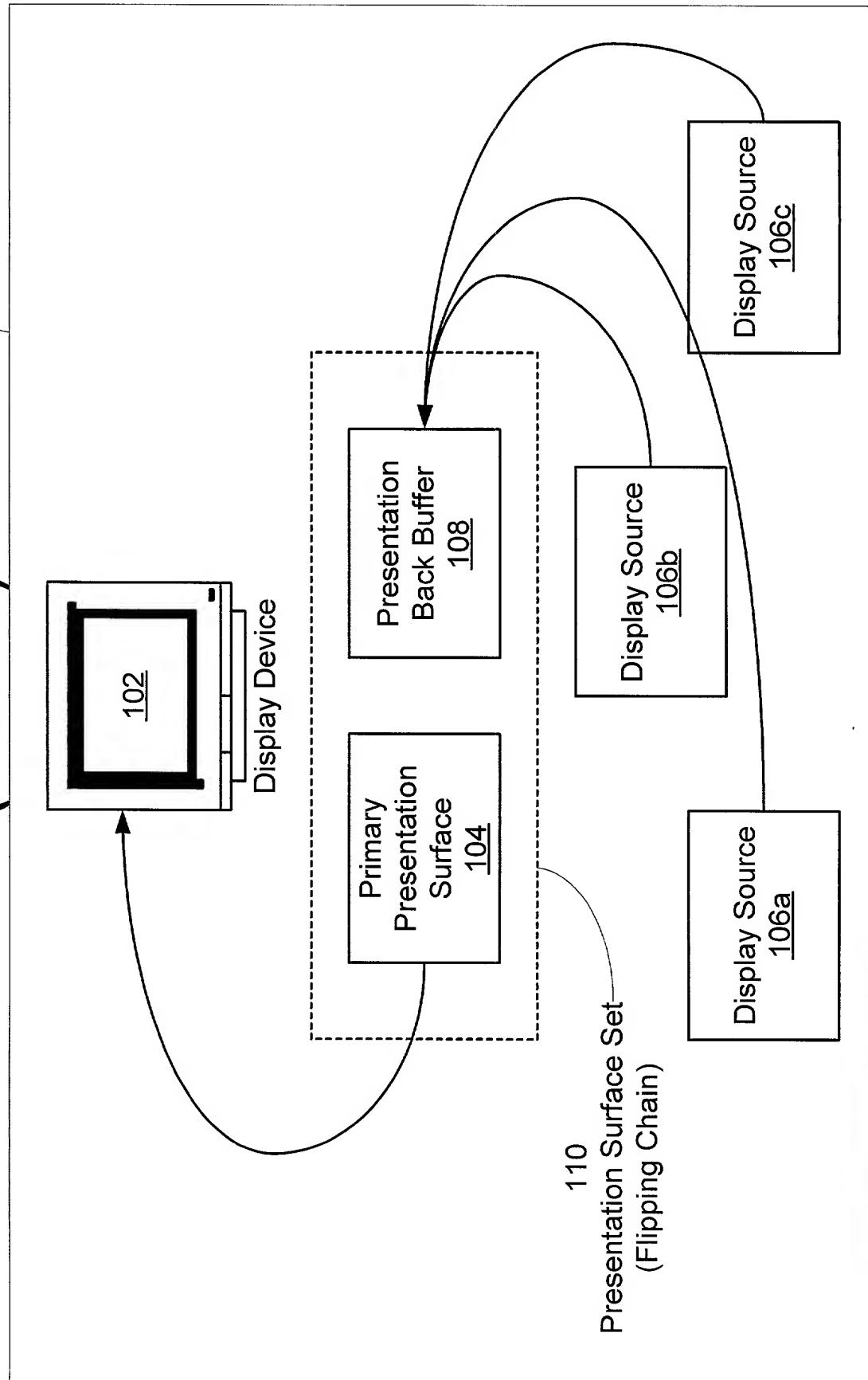


FIG. 2a (Prior Art)

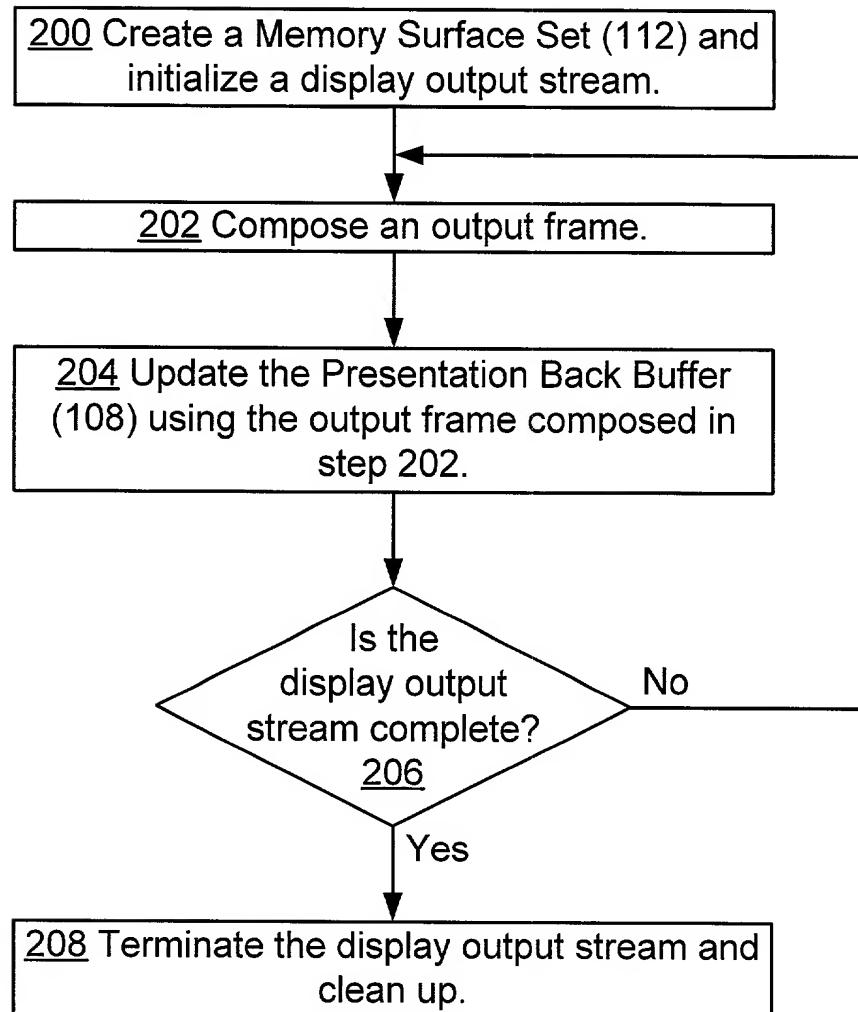


FIG. 2b (Prior Art)

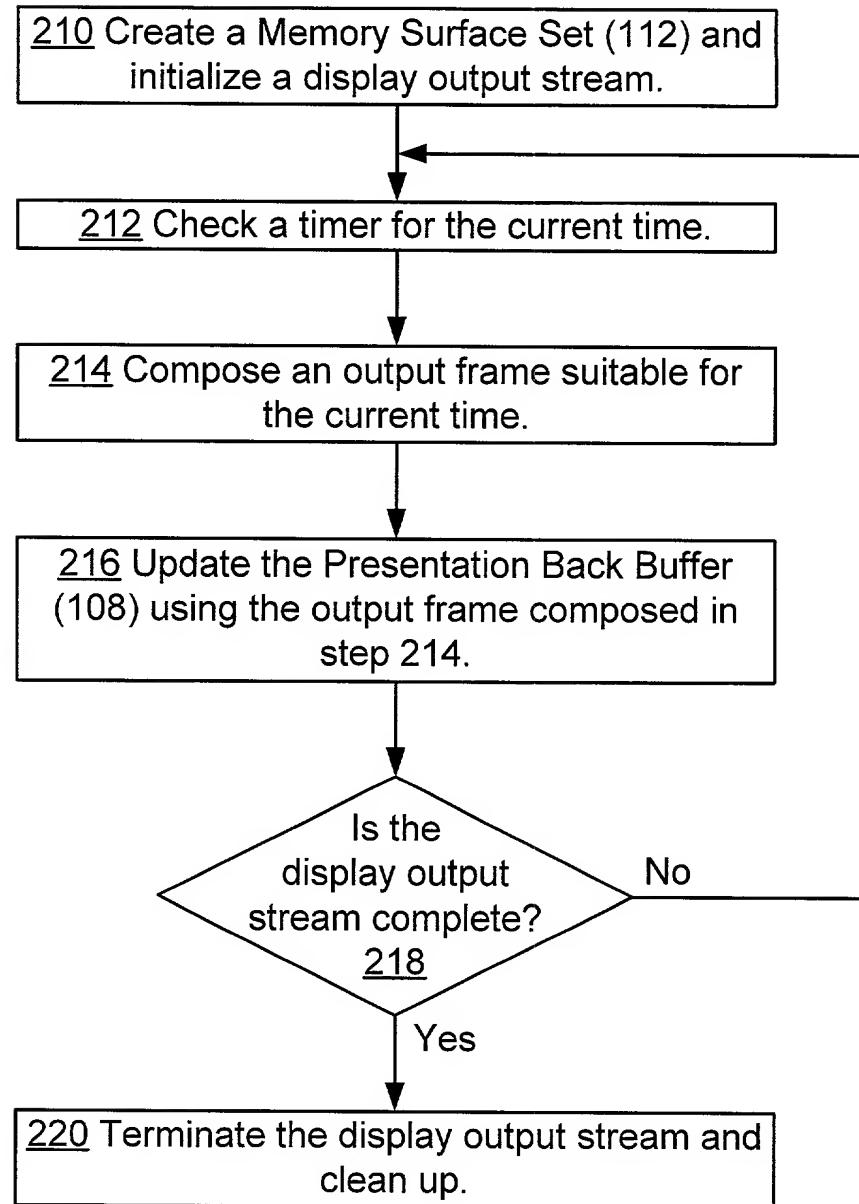


FIG. 2c (Prior Art)

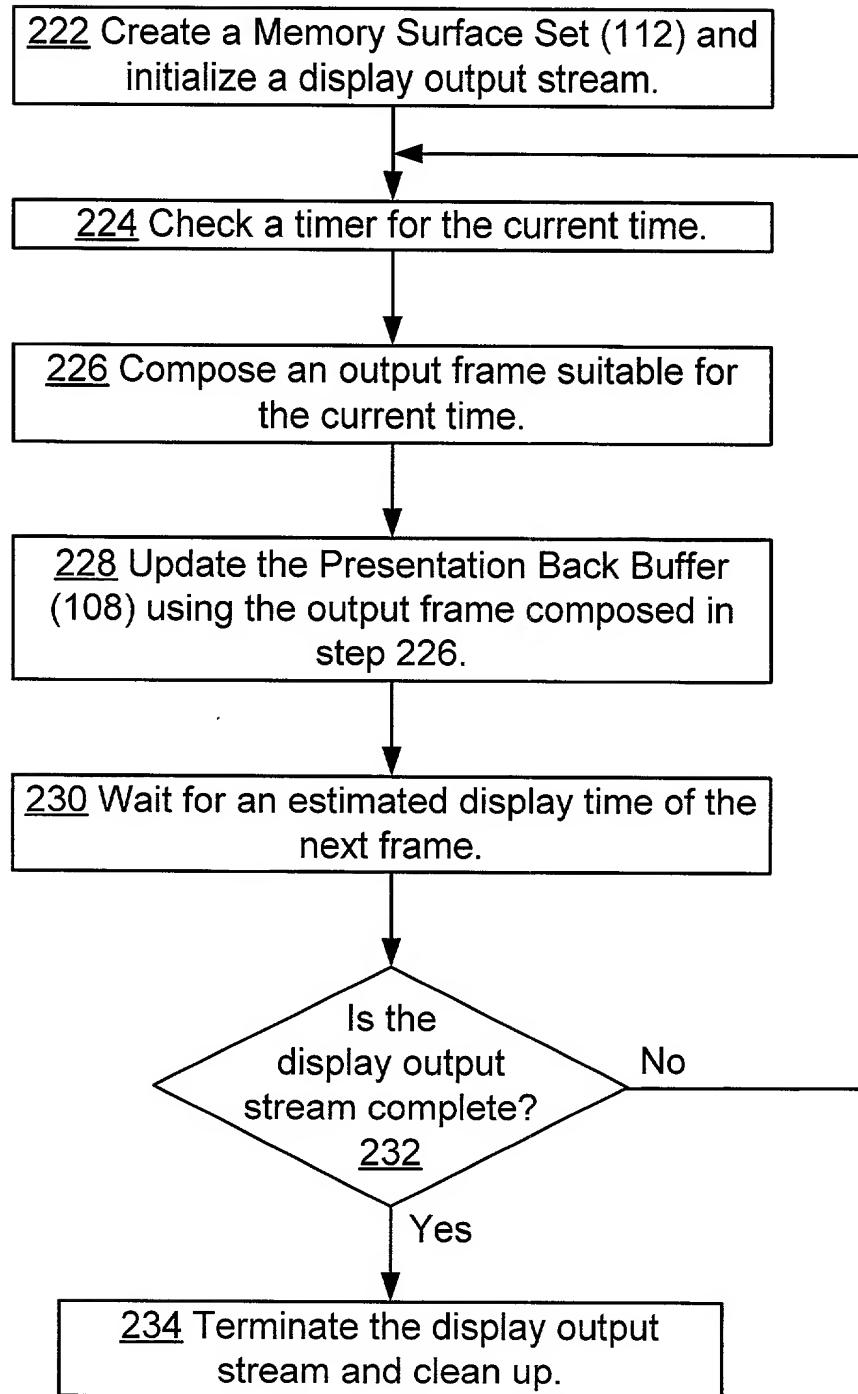


FIG. 3

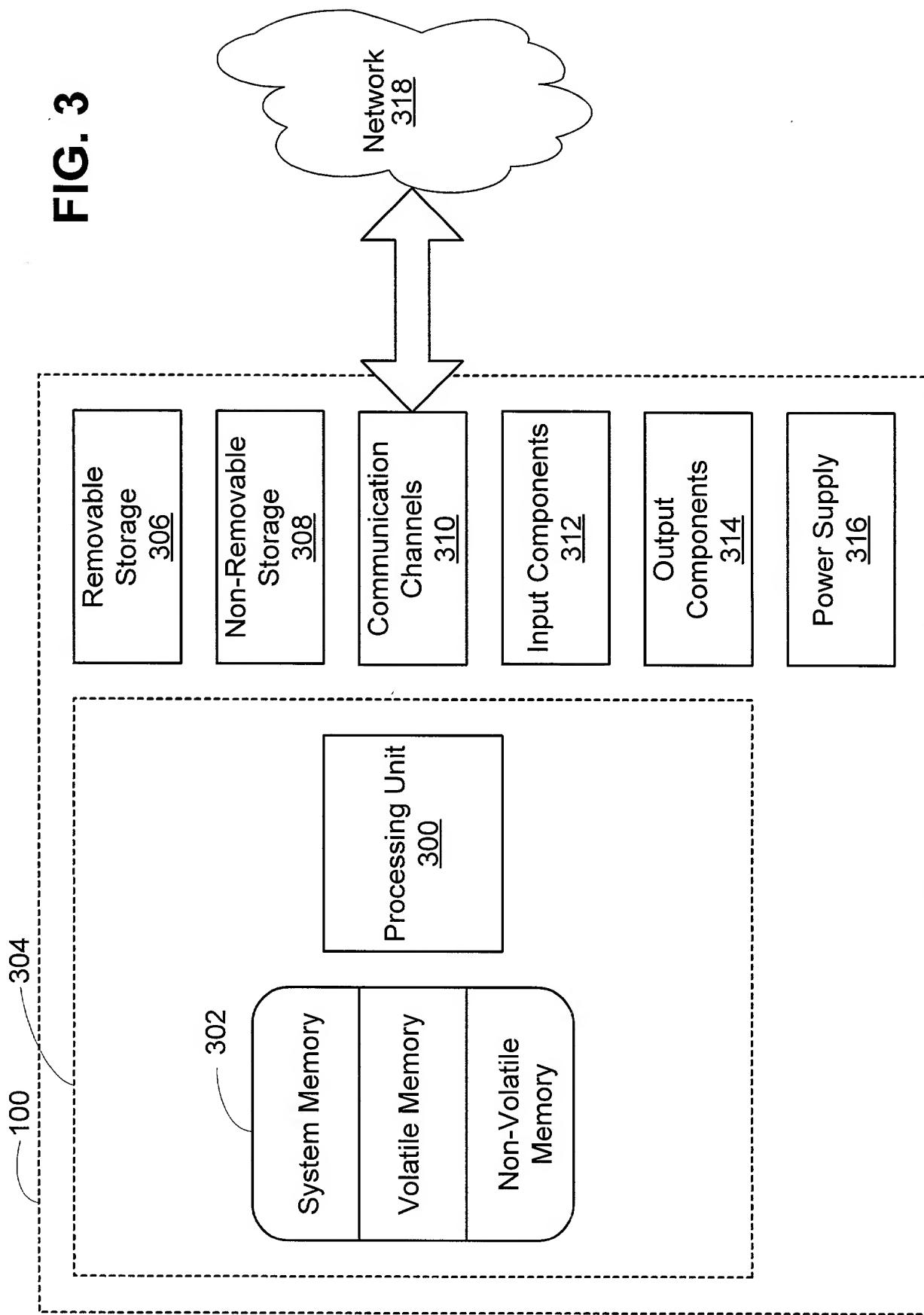


FIG. 4

100

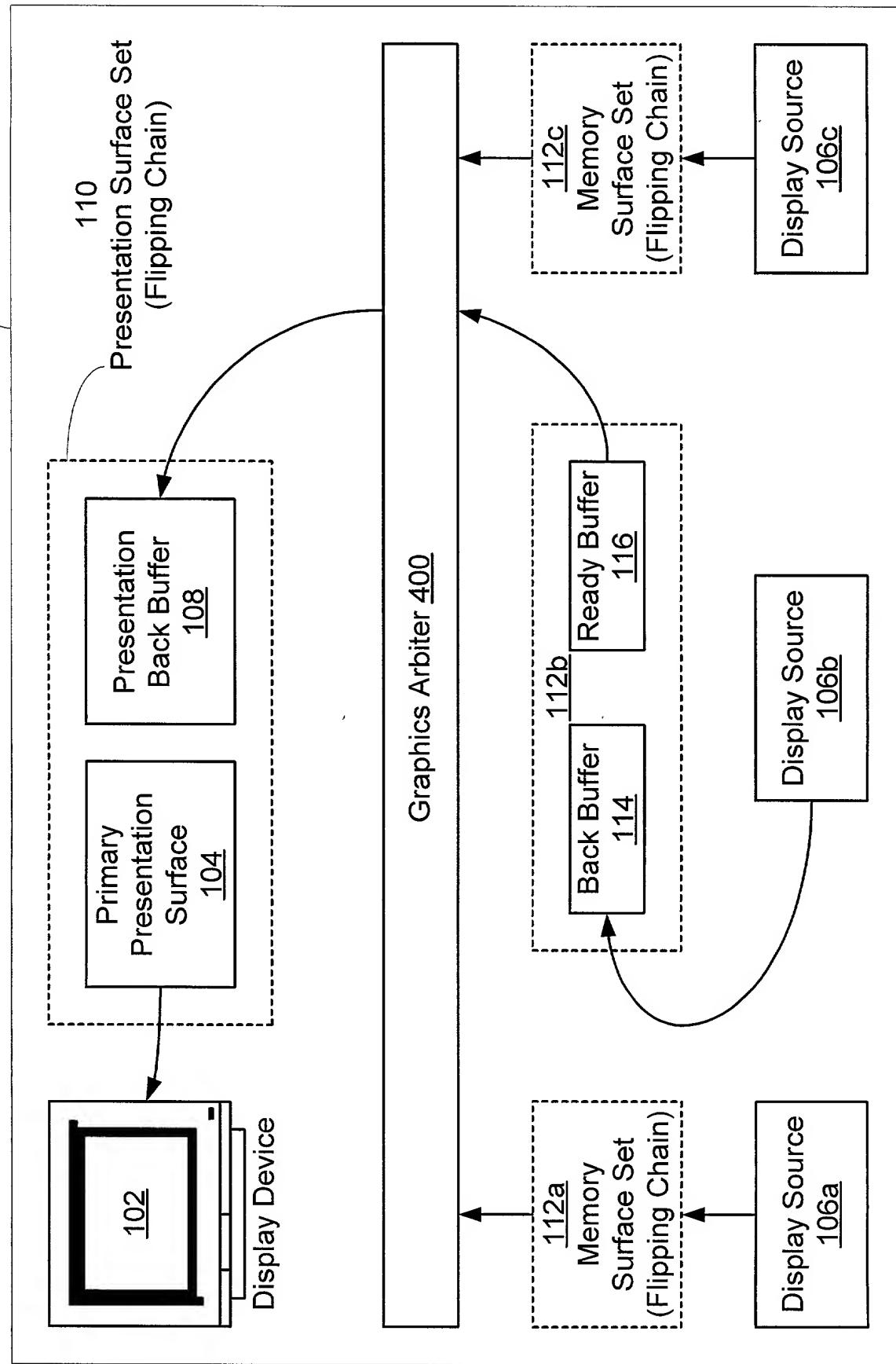


FIG. 5

100

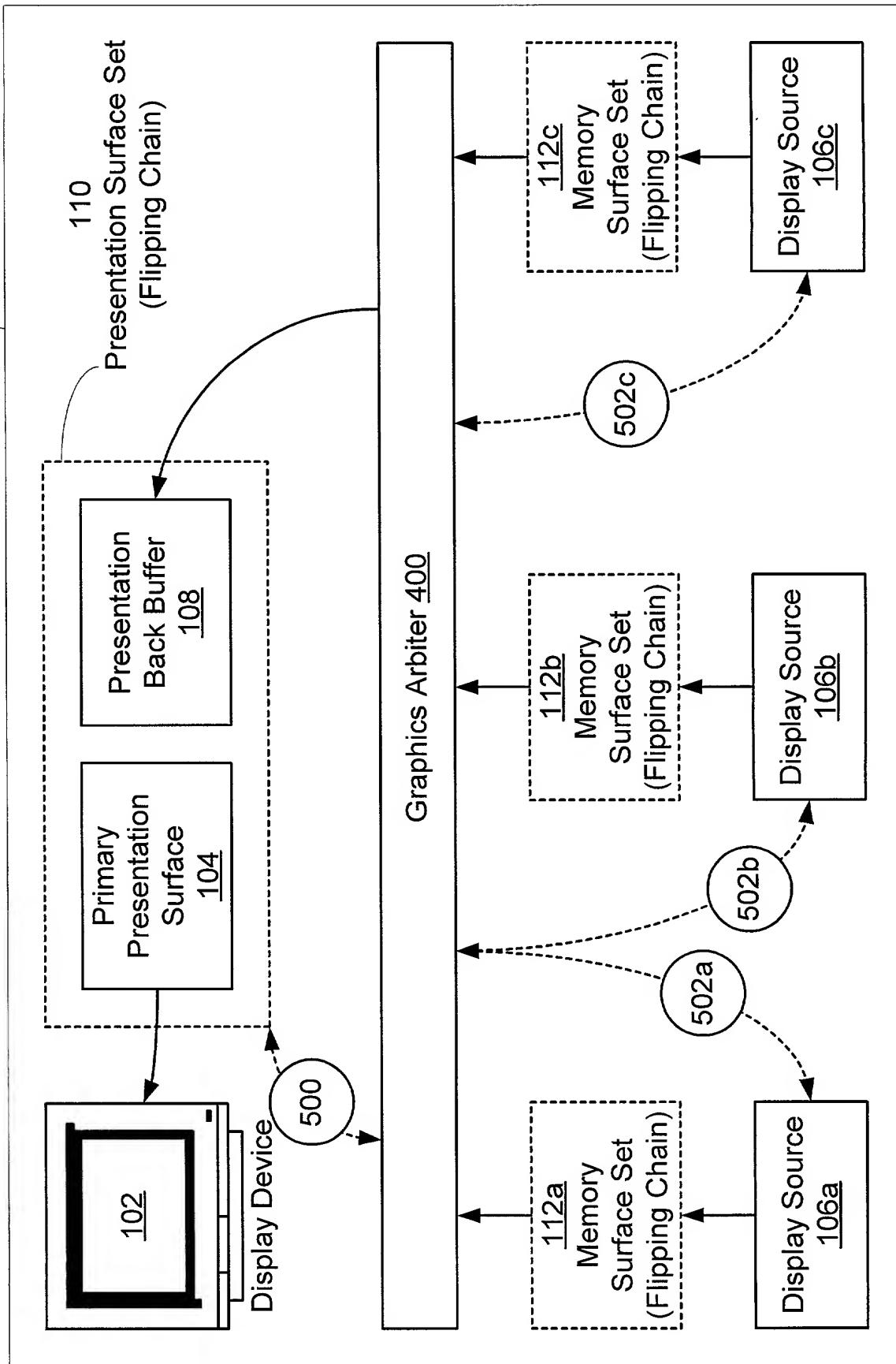


FIG. 6

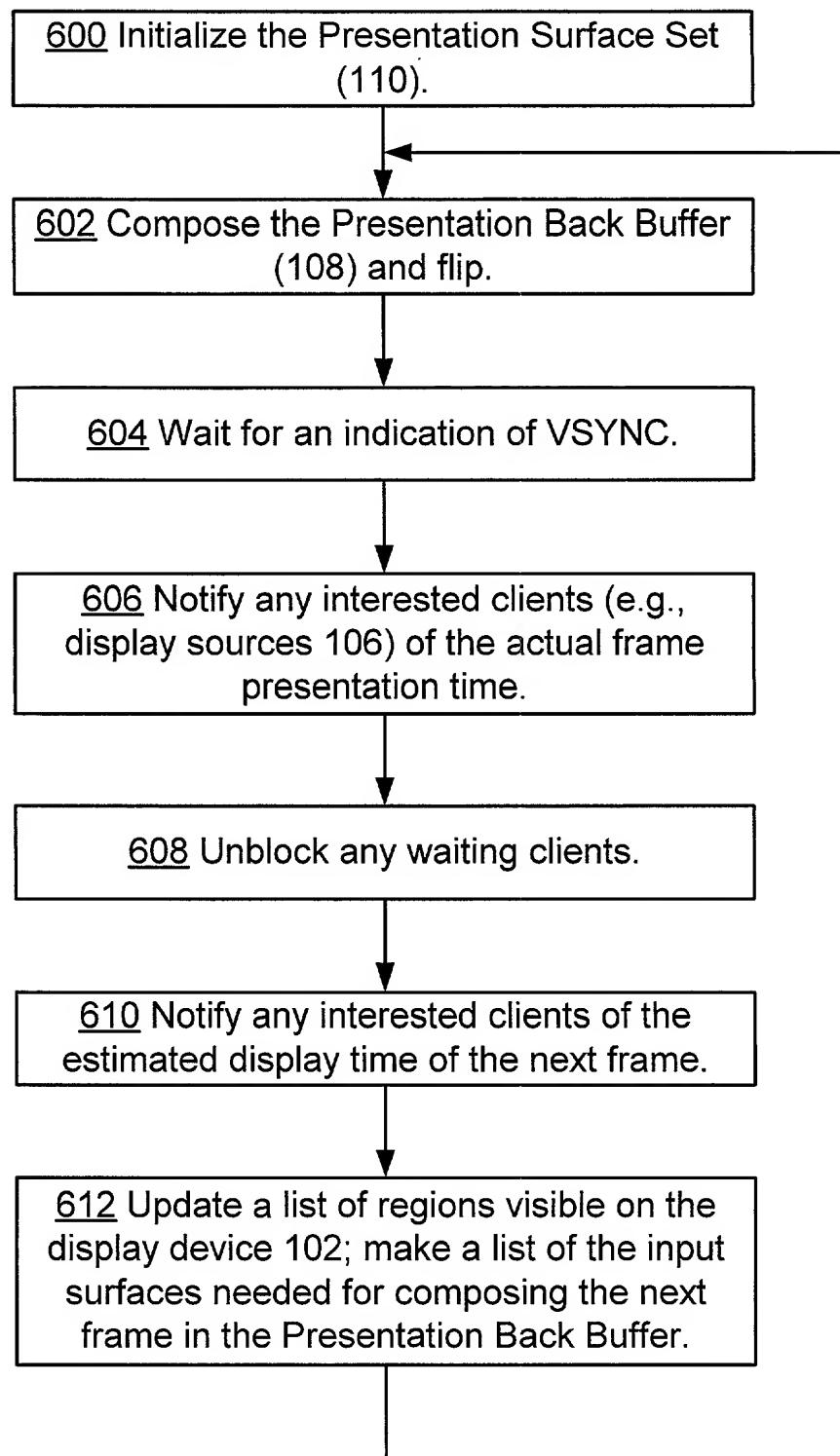


FIG. 7a

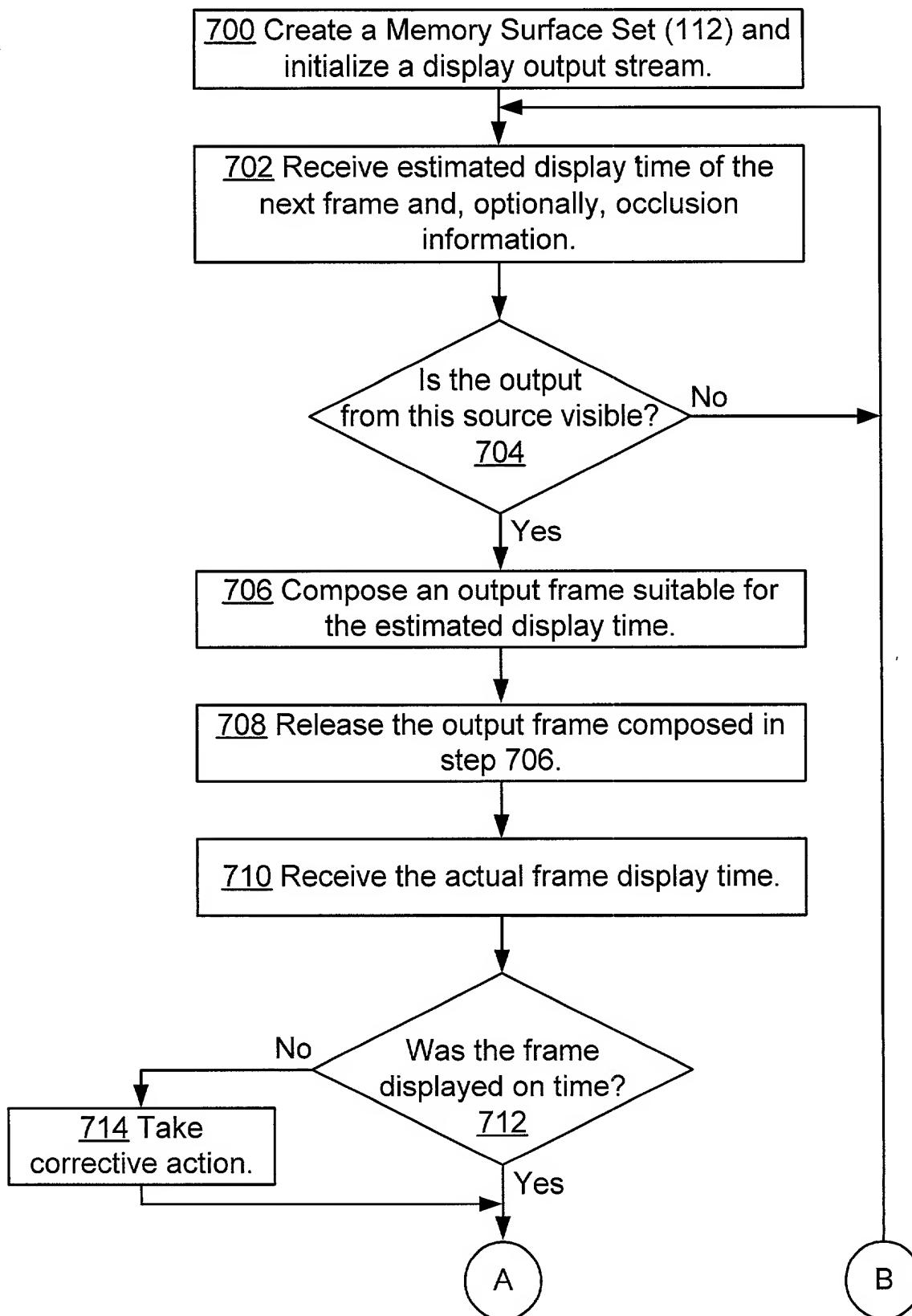


FIG. 7b

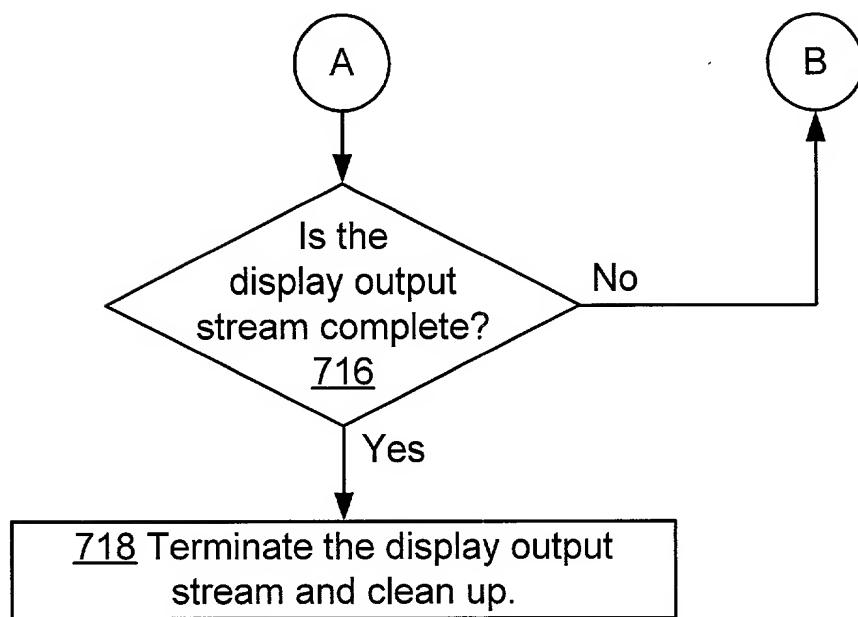


FIG. 8

100

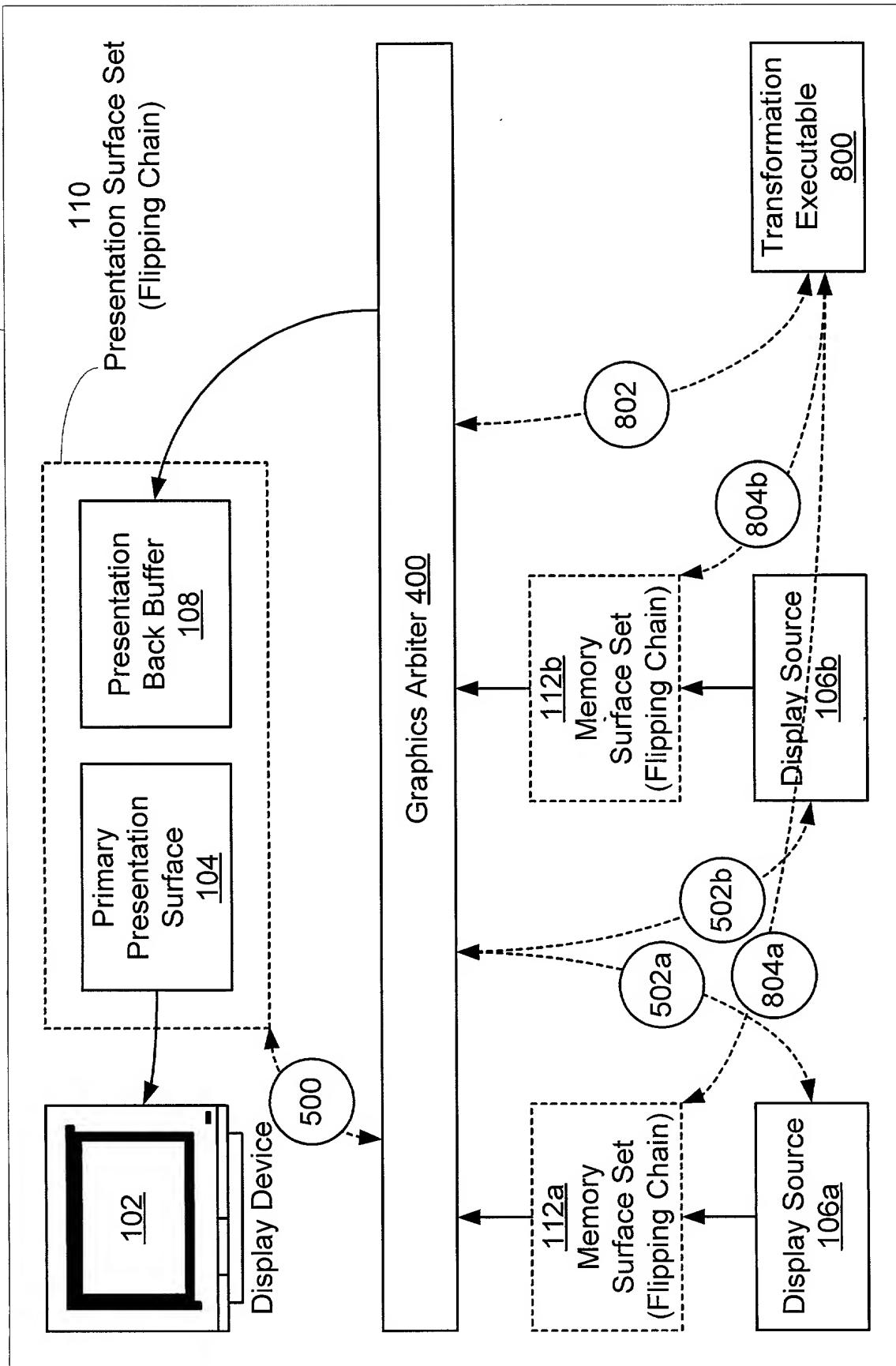


FIG. 9

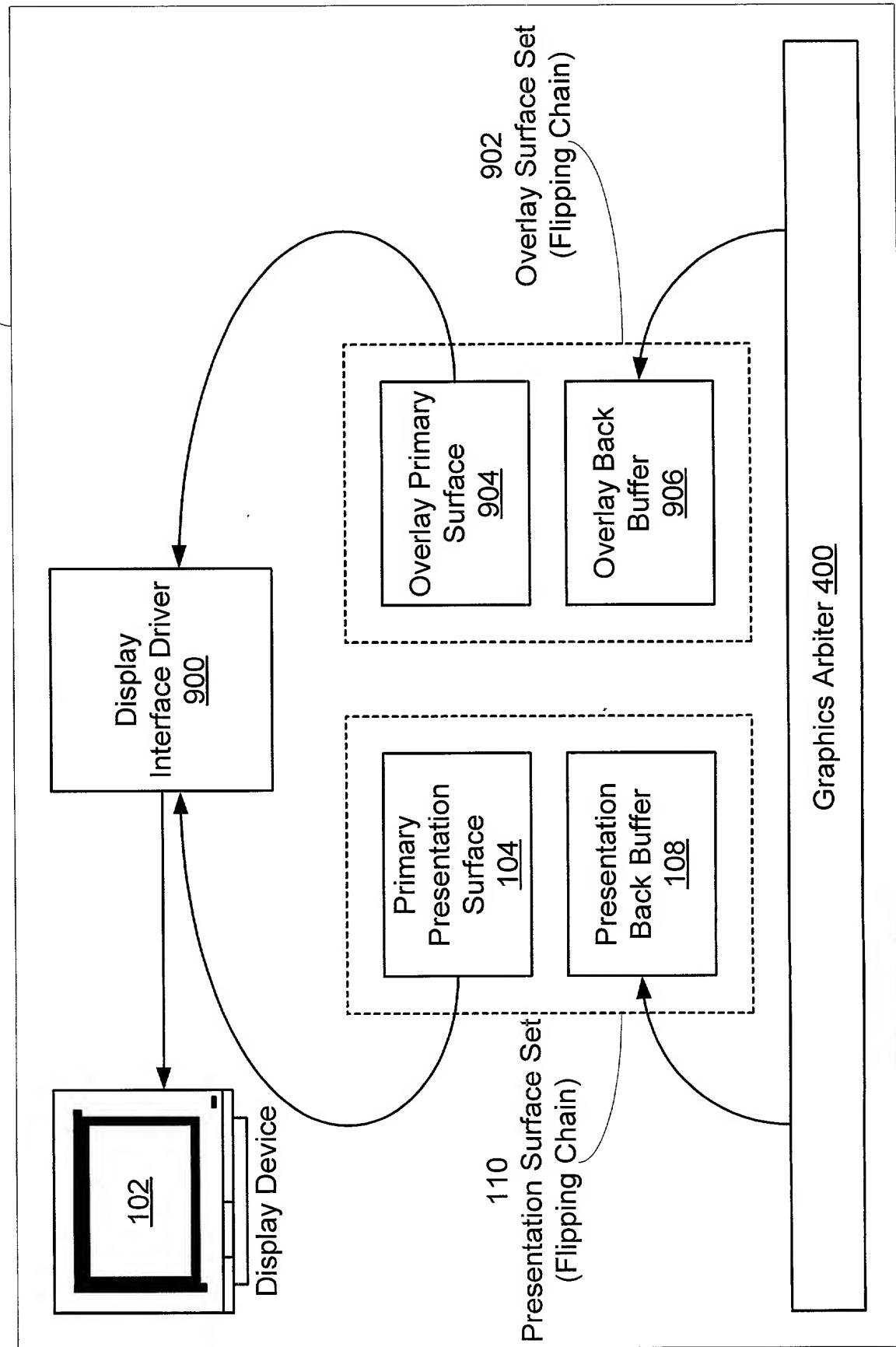


FIG. 10

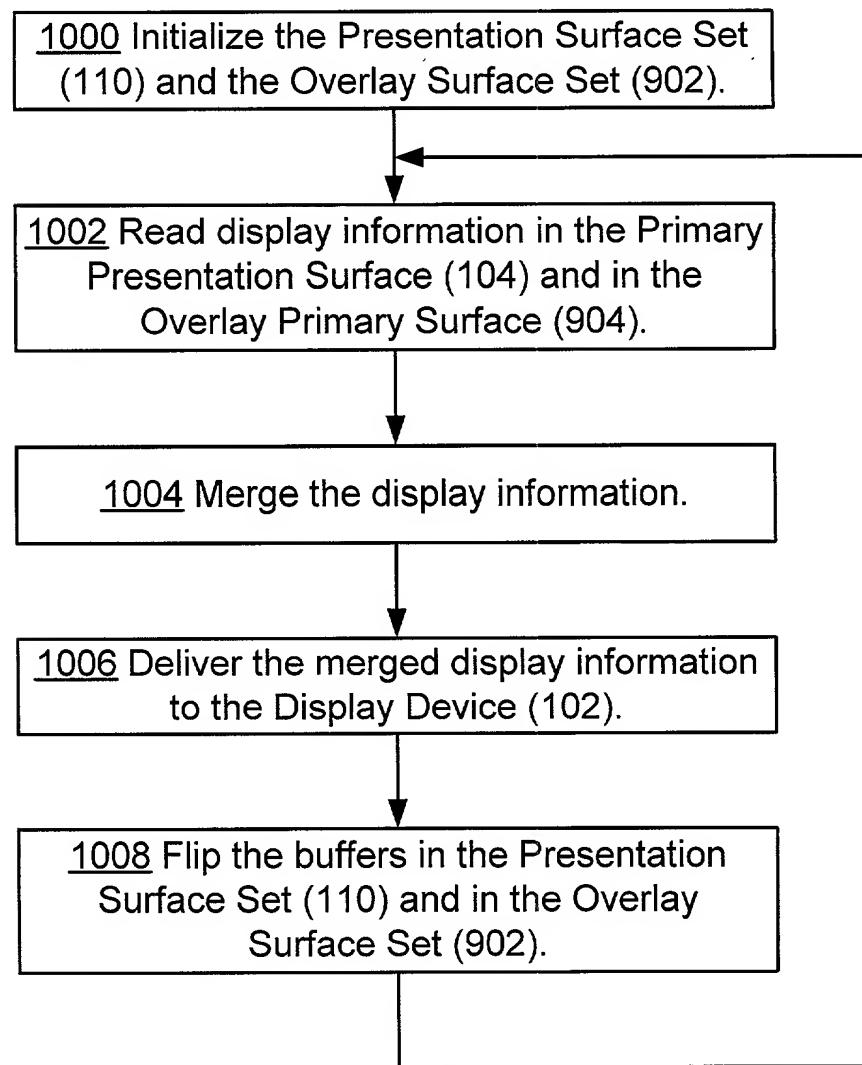


FIG. 11

